

3U VPX Graphics



**RUGGED HIGH PERFORMANCE COMPUTING & VIDEO I/O
BOARDS, MODULES & SYSTEMS FOR DEFENSE & AEROSPACE**
Capture • Process • Encode • Display • AI-Inference

Product Name	GPU/APU	Processing Speed (TFLOPS)	Memory (GB)	Outputs	Outputs
VPX3U-RTX5000E-CV	NVIDIA RTX5000	10.9	16	4	2 SDI, 2 Analog
VPX3U-RTX3000E-CV	NVIDIA RTX3000	6.4	6	4	2 SDI, 2 Analog
VPX3U-XAVIER-SBC	NVIDIA Xavier	1.4	16	3	2 SDI, 2 Analog
VPX3U-P5000-VO	NVIDIA P5000	6.2	16	4	
VPX3U-P3000-VO	NVIDIA RTX3000	6.4	6	4	

In Development

[VPX3U-RTX5000E-16PCIE](#)

- NVIDIA Quadro Turing TU104 GPU, 10.9 TFLOPS peak performance*, 3072 CUDA Cores, 384 Tensor Cores
- SOSA™ Aligned with support for SOSA payload slot profile 14.6.13
- PCIe interface up to Gen4, up to x16 on P1/P2
- Module power: configurable from 60 to 190W

*Peak performance is theoretical and limited based on power configuration

Overview

The VPX3U-RTX5000E-16PCIE module includes an NVIDIA® Quadro® Turing™ RTX5000 GPU for high performance embedded computing (HPEC) and a configurable PCIe Gen4 switch which can support up to 16 lanes using the P1B connector and the P2A connector. Data sent to/from the GPU via PCIe provides access to the Turing GPU's advanced parallel processing and AI inference processing capabilities. An optional fiber to PCIe interface is also available.

The default profile for this module is the SOSA payload profile (14.6.13). The configurable PCIe switch provides flexible integration options for system designers.

Unlocking the best performance requires the best cooling capability. WOLF's advanced cooling technology is designed to move heat using a low weight, high efficiency pipeline from the GPU die to the wedgelocks.



For more information Contact TPT KK

詳細はメール（sales.t@tptech.co.jp）でのお問い合わせもしくはホームページ（<http://www.tptech.co.jp/>）をご参照ください。

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